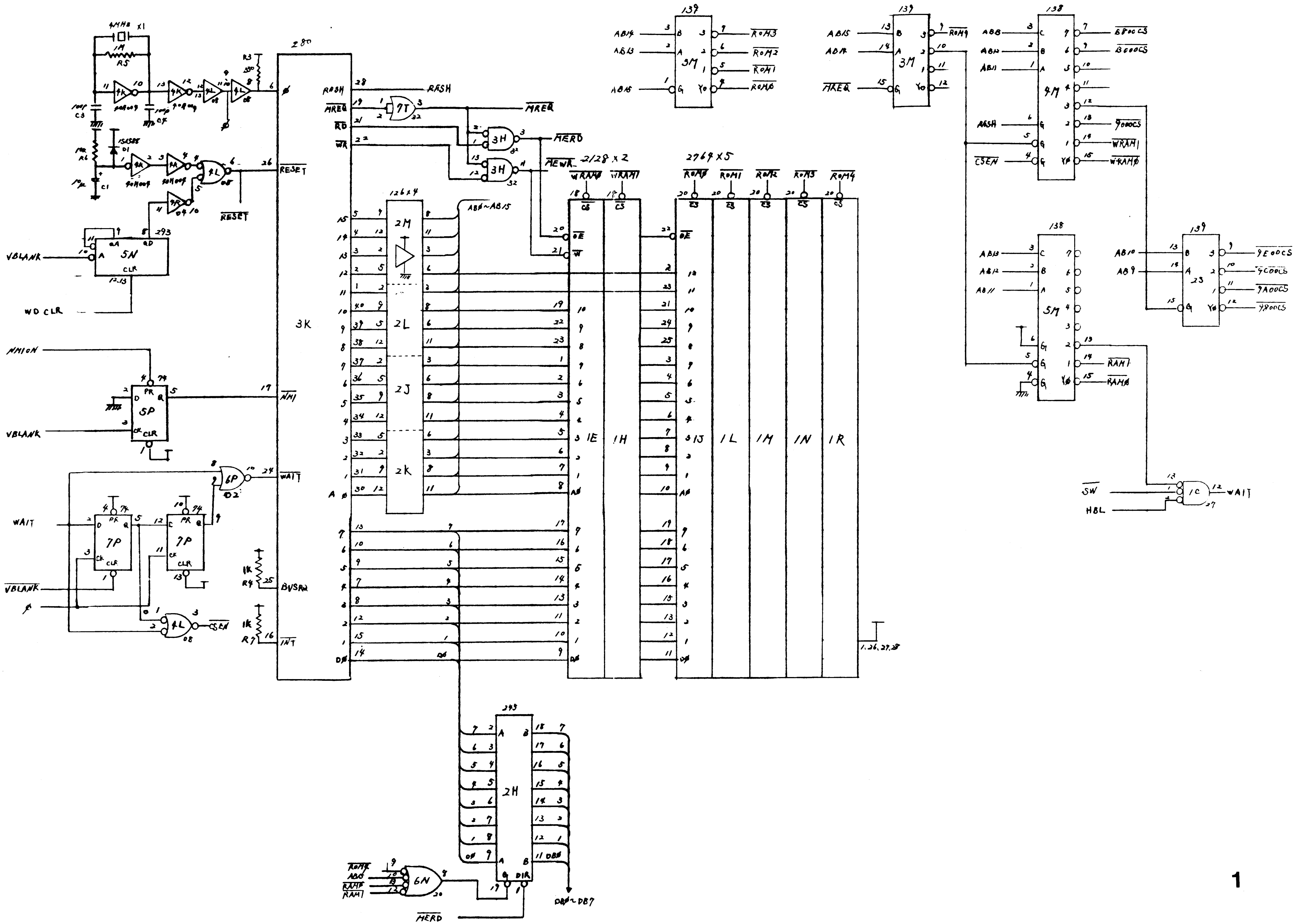
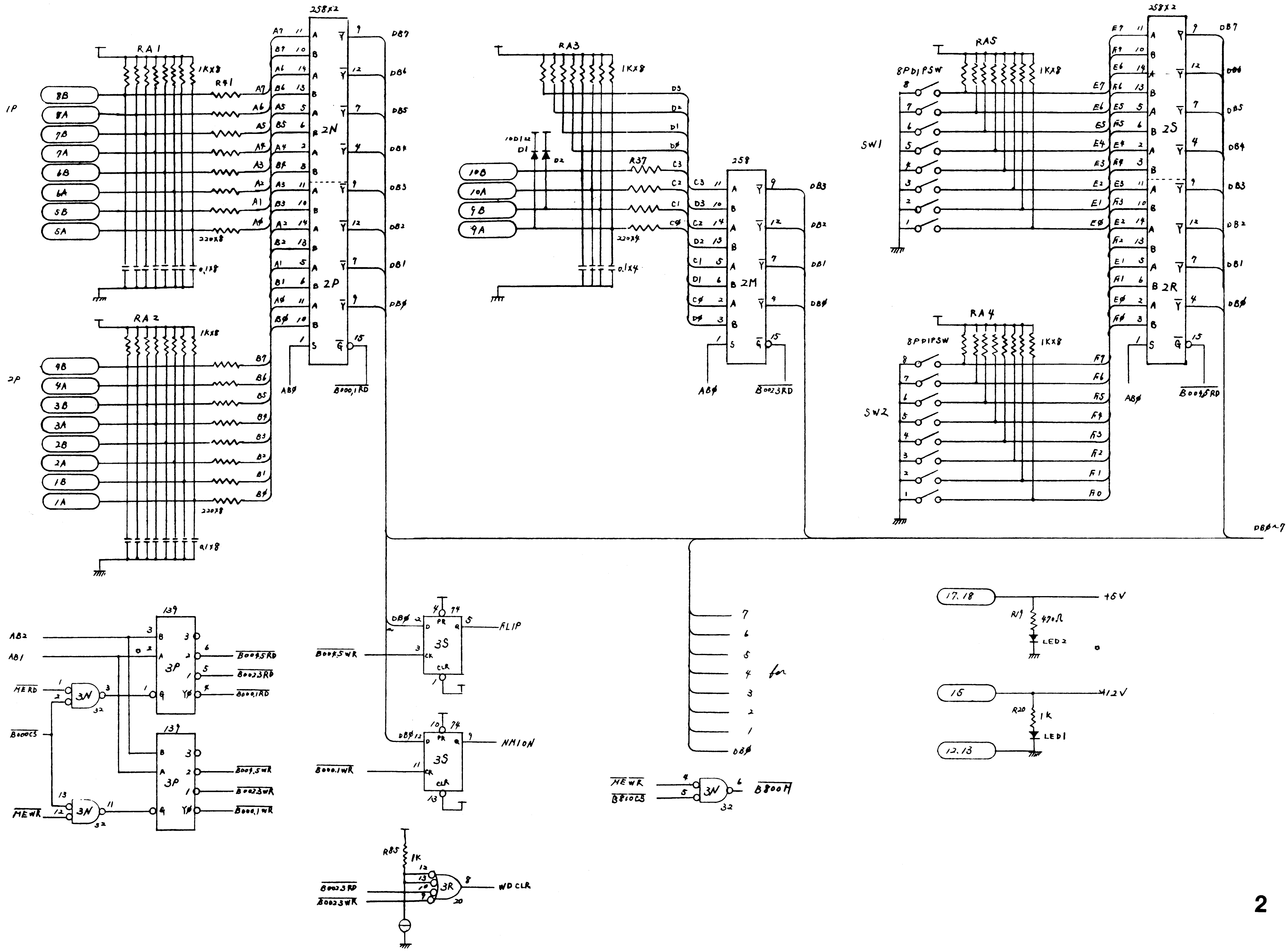
TEHKAN

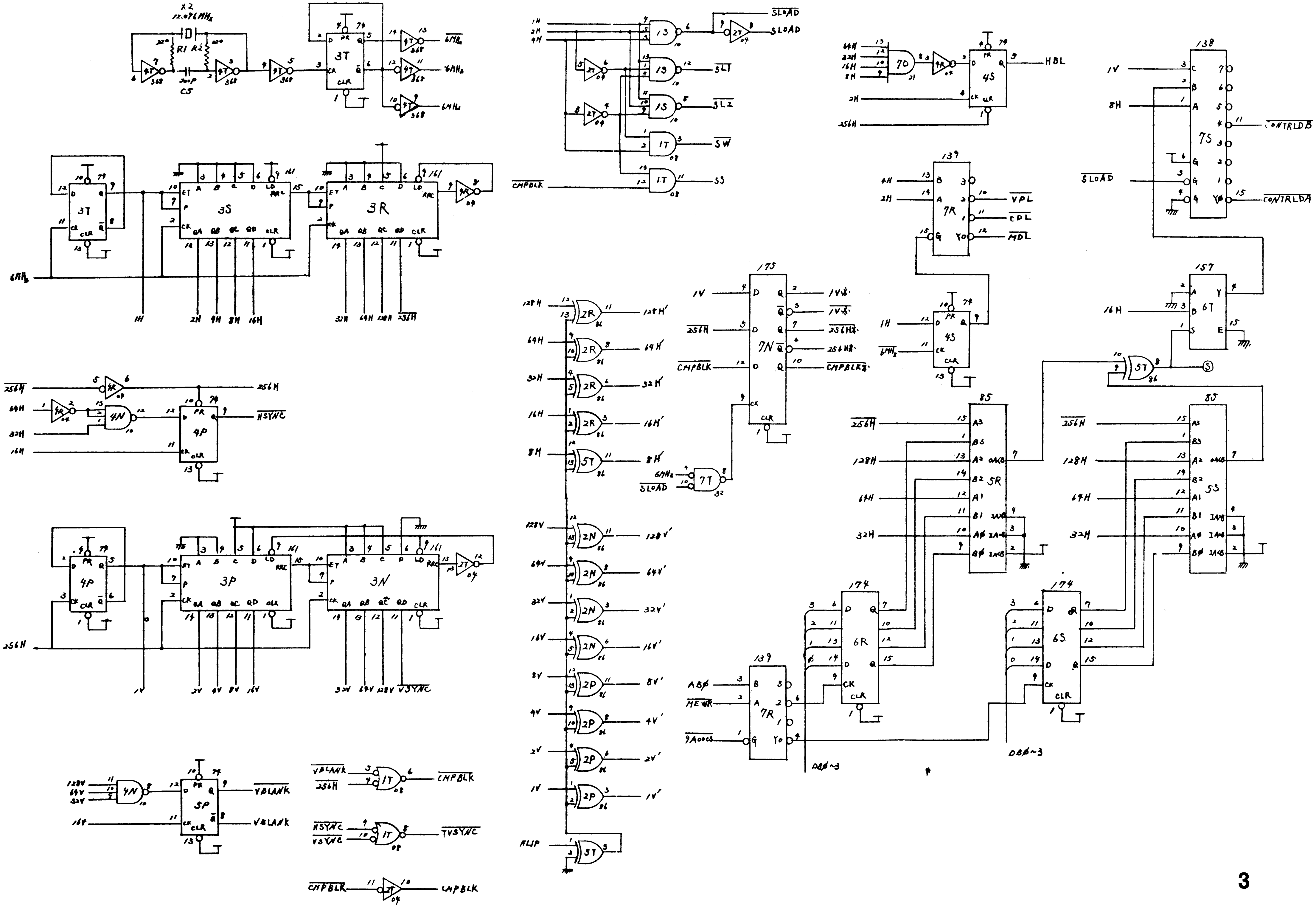
BOMB JACK™

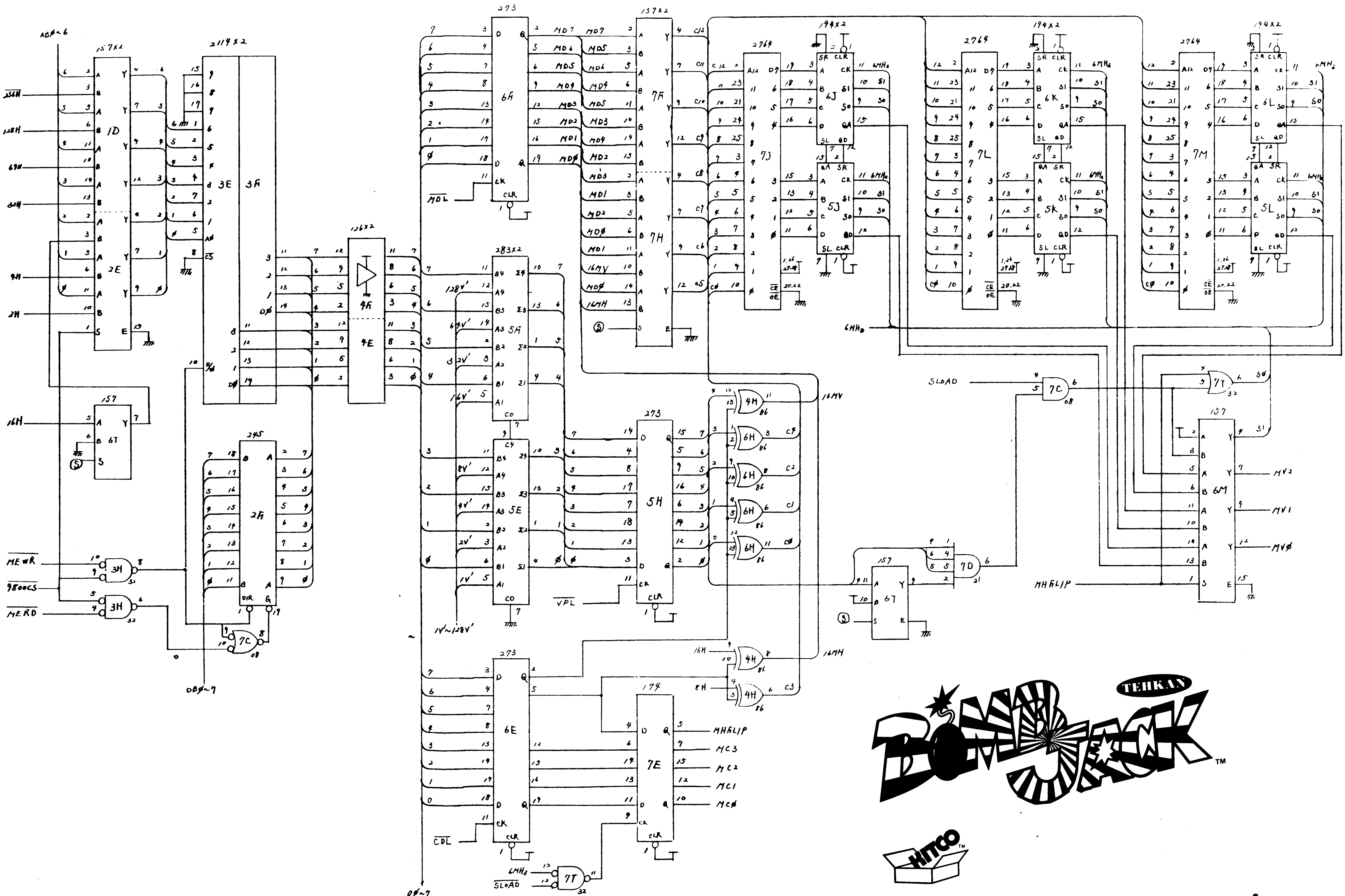
KITCO™

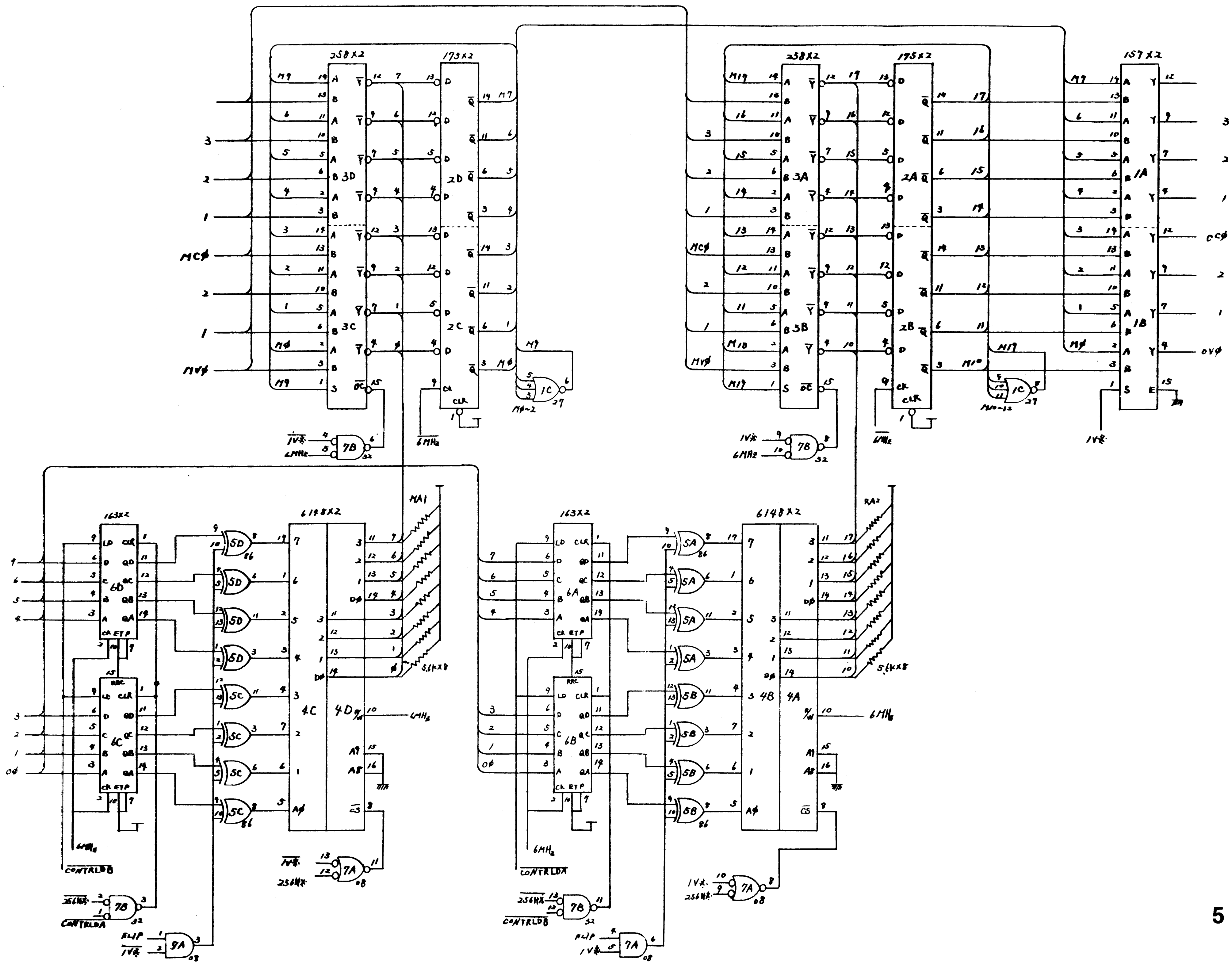

Z80
3K
RESET
NMI
WAIT
BUSRQ
INT
RFSH
MREQ
RD
WR
MERD
MEWR
2128 X 2
2716 X 5
WRAM0
WRAM1
ROM0
ROM1
ROM2
ROM3
ROM4
1E
1H
1J
1L
1M
1N
1R
2M
2L
2J
2K
2H
6N
3M
4M
5M
5N
5P
7P
6P
4L
7T
3H
1C
VBLANK
WD CLR
NMION
CSEN
SW
HBL
RESET
AB0~AB15
DB0~DB7
9E00CS
9C00CS
9A00CS
9800CS
9000CS
B800CS
B600CS
RAM1
RAM0
WRAM1
WRAM0
4MHz X1
1M R5
R3
R4
R7
D1
C1
C3
C4



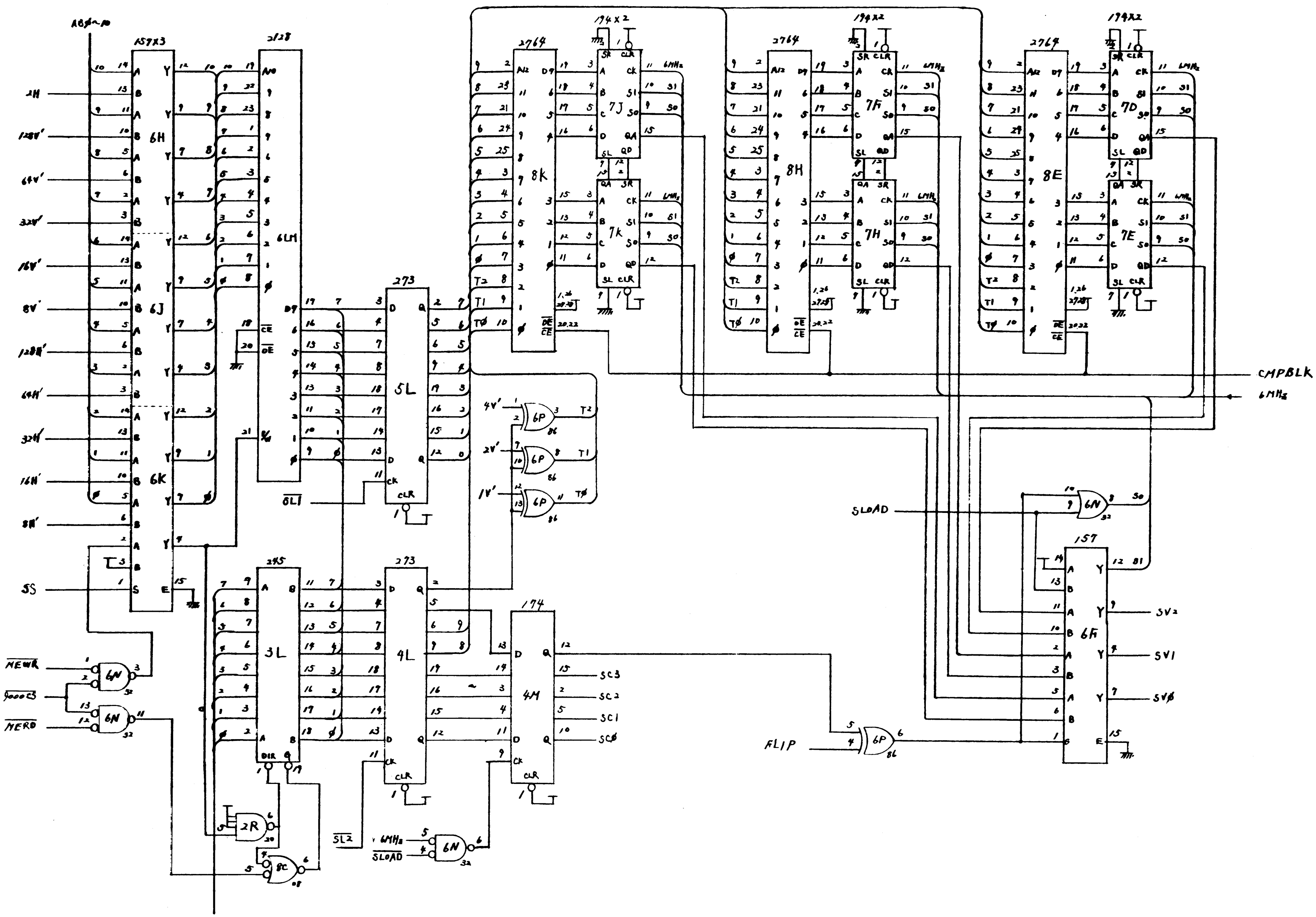
RA1
1KX8
R41
1P
8B
8A
7B
7A
6B
6A
5B
5A
220X8
0.1X8
RA2
2P
4B
4A
3B
3A
2B
2A
1B
1A
258X2
2N
2P
DB7
DB6
DB5
DB4
DB3
DB2
DB1
DBØ
ABØ
B000,1RD
RA3
10D1X2
D1
D2
R37
10B
10A
9B
9A
220X4
0.1X4
258
2M
B0023RD
RA5
8PDIPSW
SW1
RA4
SW2
2S
2R
B0045RD
DBØ~7
139
3P
AB2
AB1
MERD
B000CS
MEWR
3N
B0045RD
B0023RD
B000,1RD
B0045WR
B0023WR
B000,1WR
3S
FLIP
NMION
B0045WR
B000,1WR
R85
1K
3R
B0023RD
B0023WR
WDCLR
MEWR
B800CS
B800H
17,18
+5V
R19
470Ω
LED2
15
+12V
R20
1K
LED1
12,13


X2
12.096MHz
R1
R2
C5
3T
6MHz
4MHz
3S
3R
1H
2H
4H
8H
16H
32H
64H
128H
256H
4P
HSYNC
3P
3N
1V
2V
4V
8V
16V
32V
64V
128V
VSYNC
5P
VBLANK
CMPBLK
TVSYNC
1S
SLOAD
SLT
SL2
SW
SS
2R
5T
2N
2P
FLIP
7N
7T
7D
4S
HBL
7R
VPL
CPL
MDL
7S
CONTRLDB
CONTRLDA
6T
5R
5S
6R
6S
DBØ~3
MEWR
9A00CS

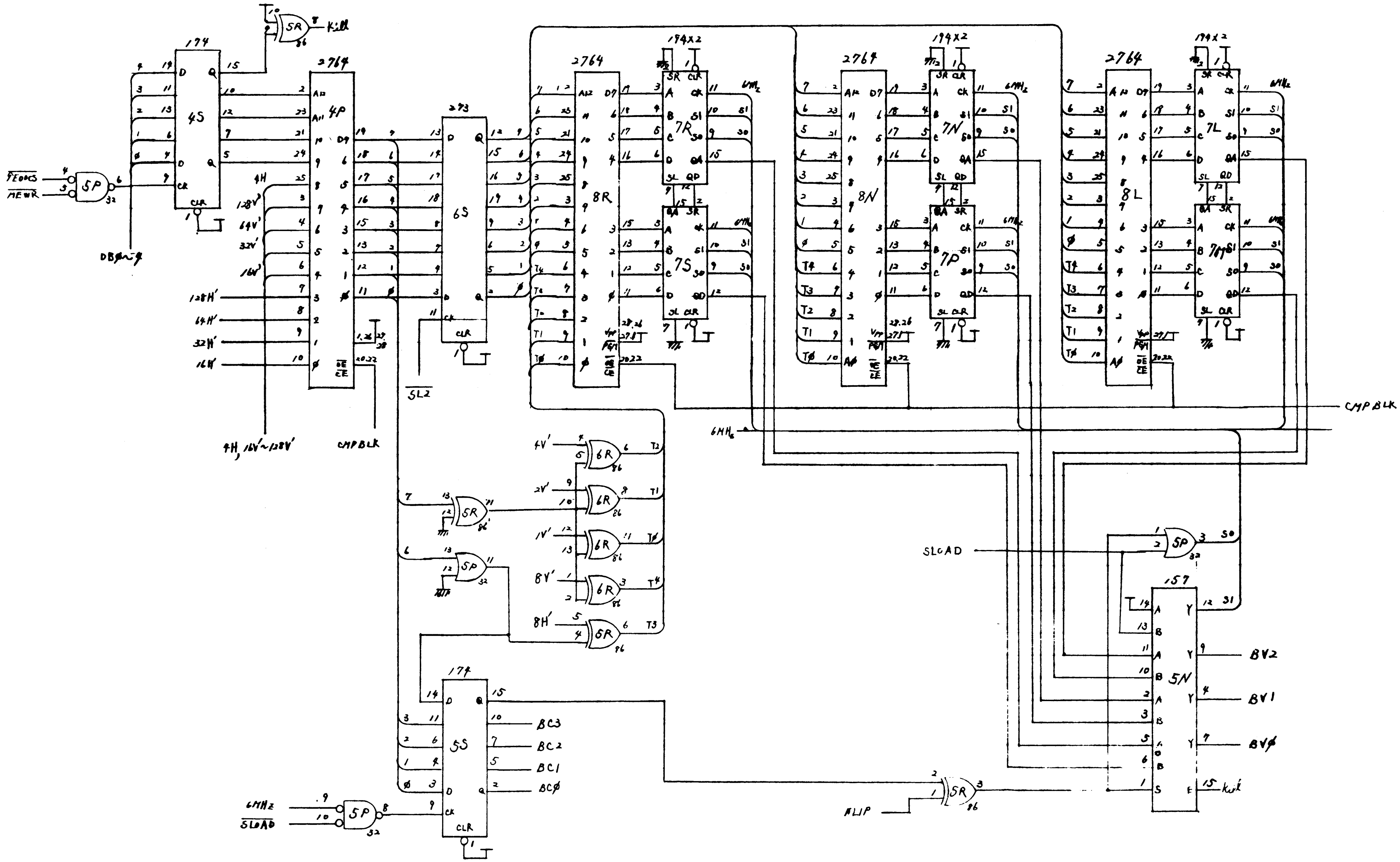

AB∅~6
157X2
2114X2
256H
128H
64H
32H
4H
2H
16H
1D
2E
3E
3A
6T
126X2
4A
4E
245
2A
MEWR
9800CS
MERD
3H
7C
DB∅~7
273
6A
MDL
283X2
5A
5E
128V'
64V'
32V'
16V'
8V'
4V'
2V'
1V'
1V~128V'
157X2
7A
7H
MD7
MD6
MD5
MD4
MD3
MD2
MD1
MD∅
16MV
16MH
5H
VPL
4H
6H
6E
CDL
174
7E
MHFLIP
MC3
MC2
MC1
MC∅
6MHz
SLOAD
7T
D∅~7
2764
194X2
7J
6J
5J
7L
6K
5K
7M
6L
5L
6MHz
SLOAD
7D
6M
MV2
MV1
MV∅
S∅
S1
16H
8H
TEHKAN
BOMB JACK
KITCO



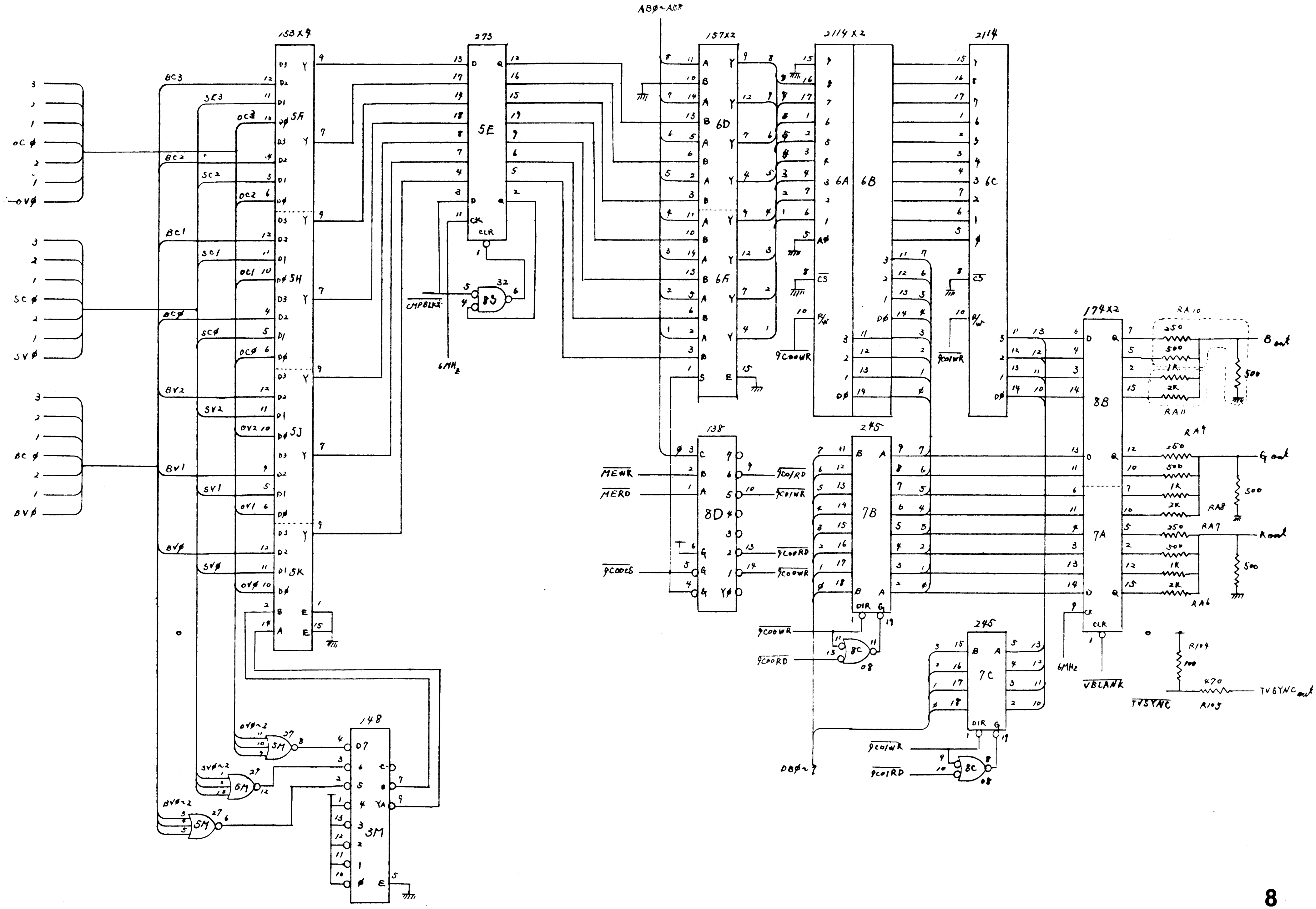
258X2
175X2
3D
2D
3C
2C
1C
7B
258X2
175X2
157X2
3A
2A
1A
3B
2B
1B
163X2
6D
6C
5D
5C
6148X2
4C
4D
7A
9A
MA1
5.6KX8
163X2
6A
6B
5A
5B
6148X2
4B
4A
RA2
5.6KX8
6MHz
CONTRLDB
CONTRLDA
256H
FLIP

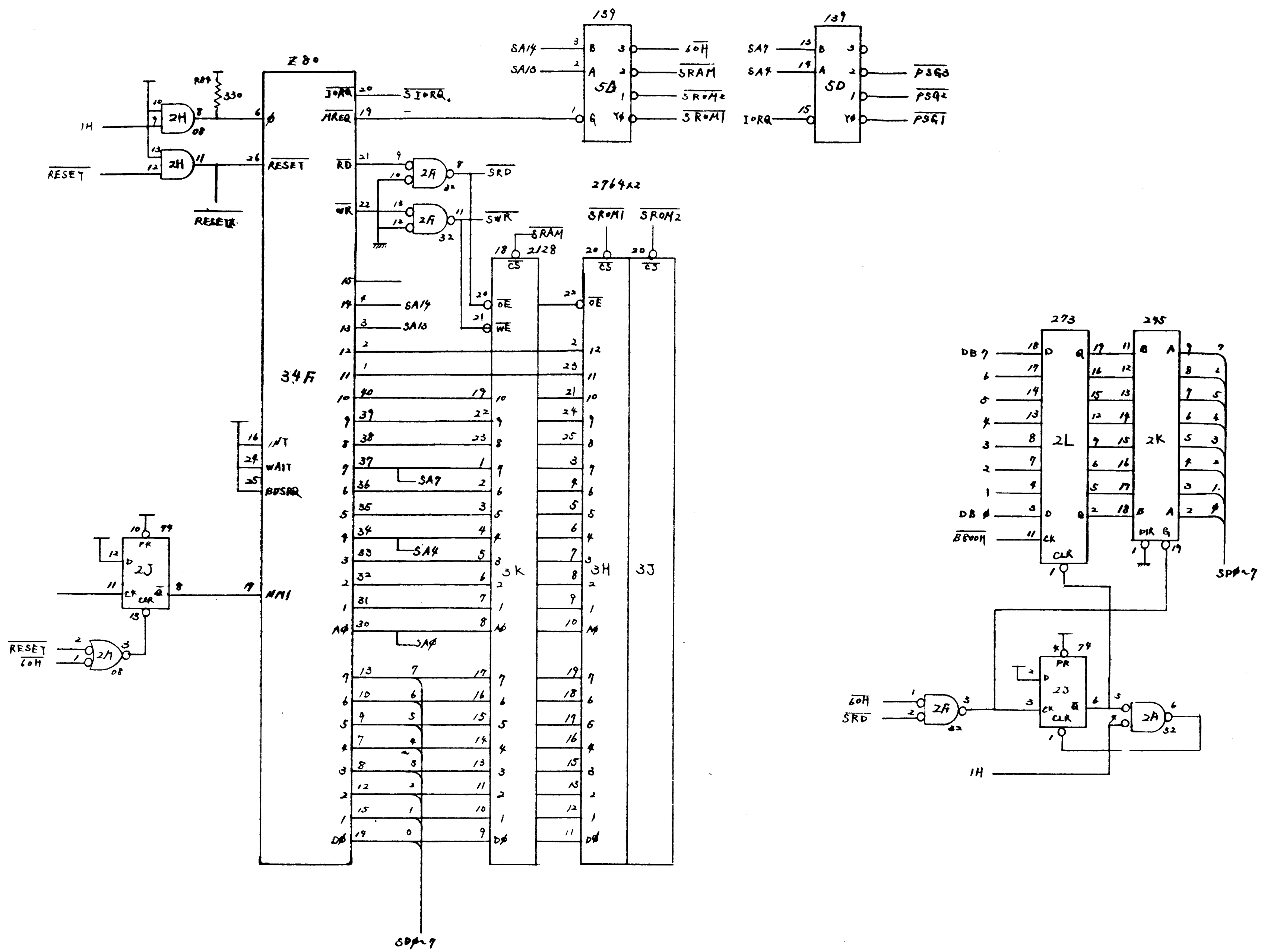


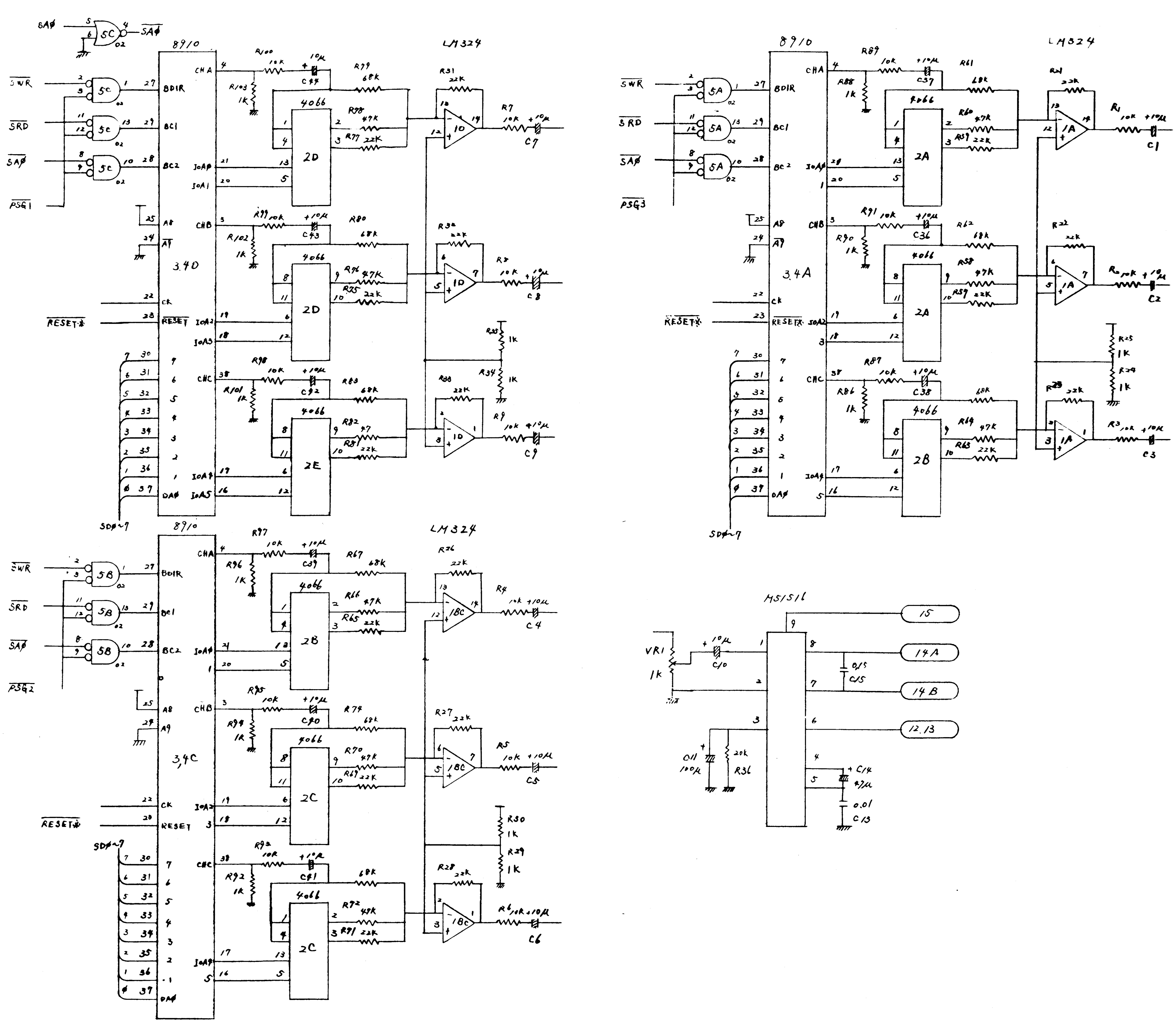
157X3
2128
6H
6J
6K
6LM
2H
128V'
64V'
32V'
16V'
8V'
128H'
64H'
32H'
16H'
8H'
SS
MEWR
MERD
273
5L
OL1
4V'
2V'
1V'
6P
T2
T1
TØ
2764
8K
8H
8E
194 X 2
7J
7K
7F
7H
7D
7E
CMPBLK
6MHz
SLOAD
6N
157
6F
SV2
SV1
SVØ
FLIP
245
3L
4L
174
4M
SC3
SC2
SC1
SCØ
2R
8C
SL2



KILL
174
4S
2764
4P
273
6S
8R
7R
7S
194X2
8N
7N
7P
8L
7L
5P
5R
6R
5S
157
5N
FEOOCS
MEWR
DB
4H
128V'
64V'
32V'
16V'
128H'
64H'
32H'
16H'
4H, 16V'~128V'
CMPBLK
SL2
4V'
2V'
1V'
8V'
8H'
T2
T1
T4
T3
6MHz
S1
S0
SLOAD
BC3
BC2
BC1
BCØ
BV2
BV1
BVØ
FLIP


AB∅~AB7
158 X 4
273
157X2
2114 X 2
2114
5F
5H
5J
5K
5E
6D
6F
6A
6B
6C
8J
CMPBLNK
6MHz
138
8D
MEWR
MERD
9COORD
9COOWR
9CO1RD
9CO1WR
9COOCS
245
7B
8C
7C
174X2
8B
7A
RA10
RA11
RA9
RA8
RA7
RA6
B out
G out
R out
VBLANK
TVSYNC
TVSYNC out
R104
R105
148
3M
5M
6M
DB∅~7


Z80
34F
139
5B
5D
2764x2
2128
3K
3H
3J
273
245
2L
2K
2J
SRAM
SROM1
SROM2
PSG3
PSG2
PSG1
RESET
SRD
SWR
NMI
WAIT
BUSRQ
INT
MREQ
IORQ



SAØ
8910
LM324
SWR
SRD
SAØ
PSG1
BDIR
BC1
BC2
CHA
CHB
CHC
IOAØ
IOA1
IOA2
IOA3
IOA4
IOA5
A8
A9
CK
RESET
DAØ
3.4D
4066
2D
2E
C7
C8
C9
SDØ~7
PSG3
3.4A
2A
2B
C1
C2
C3
PSG2
3.4C
2B
2C
C4
C5
C6
MS1516
VR1
1K
C10
C15
C11
R36
C14
C13
15
14A
14B
12,13